

FORM PTO-1449

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Attorney Docket Number	37115/JFO/B600
Application Number	09/775,476
Filing Date	February 2, 2001
Applicant(s)	Esin Terizoglu et al.
Group Art Unit	2185 2818
Examiner Name	Not Yet Assigned PHAN

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
TP	5170375	12/08/92	Mattausch et al.	365	230.03	
TP	5752264	05/12/98	Blake et al.	711	144	
TP	5781498	07/14/98	Suh	365	230.06	
TP	5864497	01/26/99	Suh	365	72	
TP	6040999	03/21/00	Hotta et al.	365	200	
TP	6144604	11/07/00	Haller et al.	365	221	
TP	6141286	10/31/00	Vo et al.	365	230.03	
TP	6141287	10/31/00	Mattausch	365	230.03	
TP	6154413	11/28/00	Longwell et al.	365	230.03	
TP	6163495	12/19/00	Ford et al.	365	230.03	
TP	6166942	12/26/00	Vo et al.	365	63	
TP	6166986	12/26/00	Kim	365	230.06	
TP	6166989	12/26/00	Hamamoto et al.	365	233	
TP	6169701	01/02/01	Eto et al.	365	230.06	
TP	6173379	01/09/01	Poplingher et al.	711	165	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

EXAMINER INITIALS	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
TP	KIYOO ITOH, et al., TRENDS IN LOW-POWER RAM Circuit Technologies, Proceedings of the IEEE, Vol. 83, No. 4, pages 524-543, April 1995.

EXAMINER SIGNATURE	Trong Phan	DATE CONSIDERED	5/14/02
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